

## REMARKS

Reconsideration and the timely allowance of the pending claims, in view of the following remarks, are respectfully requested.

In the outstanding Office Action, the Examiner rejected claims 1-4 and 9-10, under 35 U.S.C. §112, ¶2, as allegedly being indefinite; and rejected claims 1-13, under 35 U.S.C. §102(b), as allegedly being anticipated by Sriram '277 (U.S. Patent No. 6,665,277) The Examiner also objected to claim 1 for minor informalities.

By this Amendment, independent claims 1, 4, and 11 have been amended to provide a clearer presentation of the claimed subject matter and claims 2, 3, 9 and 10 have been cancelled. Applicant submits that no new matter has been introduced. As such, claims 1, 4-8, and 11 are presented for examination, of which claims 1, 4, and 11 are independent.

Applicant notes that the claim changes to claim 1 and the cancellation of claims 2, 3, 9 and 10 have rendered the objections and rejections under §112, ¶2 of these claims moot. Accordingly, the immediate withdrawal of the objections and rejections of claims 1-4 and 9-10 is respectfully requested.

Insofar as the rejections under §102(b) are still deemed relevant in view of the claim changes, Applicant traverses these rejections for the following reasons:

### I. Rejections Under §102(b).

As noted above, independent claim 1 is directed to a method for a mobile unit to synchronize with a base station and positively recites, *inter alia*, ***selecting either odd ones or even ones of said sample signal during a first period*** to be a first period signal, and ***obtaining a first slot timing according to said first period signal*** and said primary synchronization channel. Claim 1 also positively recites ***selecting either odd ones or even ones of said sample signal during a second period different from the way during said first period*** to be a second period signal, ***obtaining a second slot timing and a slot synchronization signal according to said second period signal and said primary synchronization channel***. These features are amply

supported by the various embodiments disclosed in the written description. By way of example only, the disclosed embodiments provide that the synchronization process may include slot synchronization, frame synchronization, code-group identification, and scrambling-code identification. In performing slot synchronization, the sample signal may be selected by different ways for adjacent periods to acquire a plurality of period signals. Therefore, the operation and design of the matched filter could be simpler since fewer signals are to be processed during each period. Furthermore, more than one slot timing could be obtained from the plurality of period signals, thus a slot timing with better value could be chosen among the plurality of slot timings for using in the following synchronization steps. Hence the inaccuracy caused by the frequency differences of timing occurring during slot synchronization could be reduced, and the following synchronization steps could be accelerated.

Applicant respectfully submits that, despite the Examiner's contentions, the asserted reference, Sriram '277, fails teach or suggest each and every element of the claims, including the features identified above. In particular, Sriram '277 discloses the use of tertiary synchronization code (TSC) 512 to provide both frame synchronization and partial synchronization code group identification. (See, Sriram '277: col. 5, lines 21-67).

With this said, other than the mere disclosure of the "CHIP/SLOT SYNCH USING FSC" in FIG. 7, there is no description whatsoever of the operation or performance of the slot synchronization in Sriram '277. Equally notable, there is nothing in Sriram '277 that teaches or suggests selecting sample signals by different ways in adjacent periods, namely, *selecting either odd ones or even ones of said sample signal during a first period* to be a first period signal, and *obtaining a first slot timing according to said first period signal* and said primary synchronization channel, as required by claim 1. Nor is there anything in Sriram '277 that teaches or suggests *selecting either odd ones or even ones of said sample signal during a second period different from the way during said first period* to be a second period signal, *obtaining a second slot timing and a slot synchronization signal according to said second period signal and said primary synchronization channel*, as required by claim 1.

Thus, for at least these reasons, Applicant submits that Sriram '277 is incapable of anticipating or, for that matter, rendering claim 1 unpatentable. As such, claim 1 is clearly patentable.

Applicant further submits that because independent claims 4 and 11 recite similar patentable features as claim 1, claims 4 and 11 are also patentable. And, because claims 5-8 depend from claim 4, claims 5-8 are patentable at least by virtue of dependency as well as for their additional recitations. Accordingly, the immediate withdrawal of the rejections of claims 1, 4-8, and 11 is respectfully requested.

## II. Conclusion.

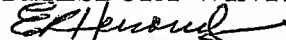
All matters having been addressed and in view of the foregoing, Applicants respectfully requests the entry of this Amendment, the Examiner's reconsideration of this application, and the immediate allowance of all pending claims.

Applicant's representative remains ready to assist the Examiner in any way to facilitate and expedite the prosecution of this matter. If any point remains in issue which the Examiner feels may be best resolved through a personal or telephone interview, please contact the undersigned at the telephone number listed below.

Please charge any fees associated with the submission of this paper to Deposit Account Number **03-3975**. The Commissioner for Patents is also authorized to credit any over payments to the above-referenced Deposit Account.

Respectfully submitted,

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